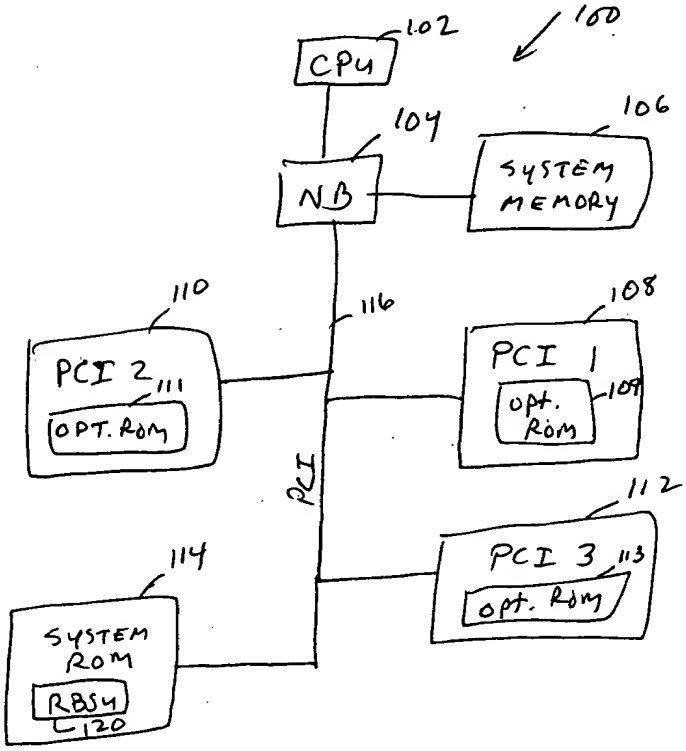
100
CPU
102
NB
104
SYSTEM MEMORY
106
PCI 2
110
111
OPT. ROM
PCI 1
108
opt. ROM
109
116
PCI
PCI 3
112
113
opt. ROM
SYSTEM ROM
114
RBSU
120

FIG. 1

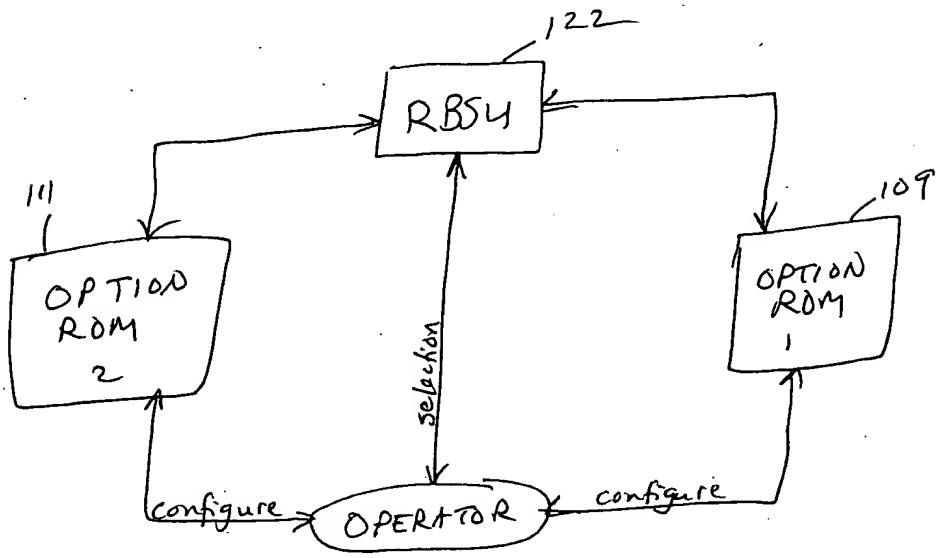
122
RBSU
111
OPTION ROM 2
selection
OPTION ROM 1
109
configure
OPERATOR
configure

FIG. 2

10/707,202

200

Values	Description	Location
$, O, R, C, _, E, N, T	8 byte signature to signify table start	Any paragraph boundary in DCH (C8000h – DFFFFh)
DW xxxxh (filled in by option ROM during POST option ROM initialization)	16 bit offset of option ROM configuration entry point	Signature offset + 8
DW xxxxh (filled in by option ROM during POST option ROM initialization)	16 bit segment of option ROM – a value of 0000h means entry point not supported	Signature offset + 10
DW 0001h	Current revision	Signature offset + 12
DW 0000h	Reserved/unused	Signature offset + 14
DD xxxxxxxxh	PCI Subsystem Vendor/Device ID	Signature offset + 16
DD yyyyyyyyh	Subsystem Vendor/Device ID mask	Signature offset + 20

202
204
206
208
210
210
214

FIG. 3